

WHAT IS CLAIMED IS:

1. A lead frame comprising:
a lead frame body comprising a sheet-shaped body
made of metal;

5 a groove portion for forming a lead which is formed
by a predetermined depth in a lead forming region on a
surface of the lead frame body; and

a lead formed so that the lead can protrude from the
groove portion onto the surface of the lead frame body,
10 the lead being made of material different from material
of the lead frame body.

2. A lead frame according to claim 1, the lead
including: a first conductor layer formed in the groove
15 portion; a second conductor layer formed on the first
conductor layer; and a third conductor layer formed on
the second conductor layer, wherein

the first conductor layer is assembled to an
assembling member, and

20 the third conductor layer is assembled to a bonding
pad of a semiconductor chip.

3. A lead frame according to claim 2, wherein the
first conductor layer covers an entire inner wall of the
25 groove portion.

4. A lead frame according to claim 2, wherein an
interface of the first and the second conductor layer is
located above the surface of the lead frame body.

30 5. A lead frame according to claim 1, wherein the
lead includes a barrier layer for suppressing a reaction
between the lead frame body and the first conductor
layer, the barrier layer being provided between the first
35 conductor layer and the groove portion.

6. A lead frame according to claim 2, wherein the first conductor layer is made of metal capable of forming an alloy together with solder.

5 7. A lead frame according to claim 2, wherein the film thickness of the first and the second conductor layer is 0.5 to 2 μm .

8. A lead frame according to claim 2, wherein the first conductor layer comprises a gold layer.

9. A lead frame according to claim 2, wherein the third conductor layer comprises a gold layer.

15 10. A lead frame according to claim 2, wherein the second conductor layer comprises a metallic layer, the primary component of which is nickel.

11. A method of manufacturing a lead frame comprising:

a step of forming a resist pattern on a surface of the lead frame body comprising a sheet-shaped body made of metal so that a lead forming region can be opened;

25 a step of forming a groove portion for forming a lead of a predetermined depth in the lead forming region when etching is conducted while the resist pattern is being used as a mask; and

30 a step of forming a lead, the material of which is different from the material of the lead frame body, so that the lead can protrude from the groove portion onto a surface of the lead frame body.

12. A method of manufacturing a lead frame according to claim 11, wherein the step of forming the lead includes an electrolytic plating step of forming a

metallic film on an inner wall of the groove portion while the resist pattern is being used as a mask.

13. A method of manufacturing a lead frame
5 according to claim 11, further comprising a step of shrinking the resist pattern after the groove portion is formed while the resist pattern is being used as a mask so that a surface of the lead frame body round the groove portion can be somewhat exposed.

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14. A method of manufacturing a lead frame according to claim 11, the step of forming the lead including:

15 a step of forming a first conductor layer in the groove portion and in its periphery exposed from the resist pattern which has shrunk in the shrinking step;

a step of forming a second conductor layer, which is laminated so that an end edge of the first conductor layer can be left, on the first conductor layer; and

20 a step of forming a third conductor layer on the second conductor layer.

15. A method of manufacturing a lead frame according to claim 14, the step of forming the second
25 conductor layer including:

a step of forming a thin conductive film on the first conductor layer; and

a step of conducting etch-back on the thin conductive film by means of anisotropic etching.

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16. A method of manufacturing a lead frame according to claim 11, wherein the step of forming the lead includes a step of forming the first to the third conductor layer in the groove portion in order, and an
35 interface of the first and the second conductor layer is located above the surface of the lead frame body.

17. A method of manufacturing a lead frame according to claim 11, wherein the step of forming the lead includes a step of forming a barrier layer for
5 suppressing a reaction of the lead frame body and the first conductor layer, between the first conductor layer and the groove portion.

18. A method of manufacturing a lead frame
10 according to claim 11, wherein the step of forming the groove portion includes an anisotropic etching step for forming a groove, the cross section of which is a rectangle of 0.5 to 2.5 μm depth.

19. A semiconductor device comprising:
a semiconductor chip;
a lead connected to the semiconductor chip; and
a piece of sealing resin, wherein a portion of the
reverse face of the lead protrudes from a principal plane
20 of the piece of sealing resin, and the lead is a thin film formed from the outer face side to the inner face side.

20. A semiconductor device according to claim 19,
25 the lead including: a first conductor layer; a second conductor layer laminated inside the first conductor layer; and a third conductor layer formed inside the second conductor layer, wherein an entire surface of the lead, which is exposed from the sealing resin, is covered
30 with the first conductor layer.

21. A semiconductor device according to claim 20,
wherein an interface of the first and the second
conductor layer is located inside the surface of the
35 sealing resin.

22. A semiconductor device according to claim 19, wherein the first conductor layer is made of metal capable of forming an alloy together with solder.

5 23. A semiconductor device according to claim 19, wherein the film thickness of the first and the second conductor layer is 0.5 to 2 μm .

10 24. A semiconductor device according to claim 20, wherein the first conductor layer comprises a gold layer.

25. A semiconductor device according to claim 20, wherein the third conductor layer comprises a gold layer.

15 26. A semiconductor device according to claim 20, wherein the second conductor layer comprises a metallic layer, the primary component of which is nickel.

20 27. A method of manufacturing a semiconductor device, in which a lead frame is prepared, the lead frame including a lead frame body comprising a sheet-shaped body made of metal and also including a groove portion for forming a lead, the groove portion being formed in a lead forming region on a surface of the lead frame body
25 by a predetermined depth, and also including a lead formed so that the lead can protrude from the groove portion onto a surface of the lead frame body, the lead being made of material different from the lead frame body, the method of manufacturing a semiconductor device
30 comprising:

 a semiconductor chip mounting step of mounting a semiconductor chip on a lead frame and electrically connecting the semiconductor chip to the lead;

35 a resin sealing step of covering the semiconductor chip with the sealing resin;

a step of removing the lead frame body by means of etching; and

a dicing step of dicing into individual semiconductor devices.

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28. A method of manufacturing a semiconductor device according to claim 27, the lead including: a first conductor layer formed in the groove portion; a second conductor layer laminated on the first conductor layer; and a third conductor layer formed on the second conductor layer, wherein

the first conductor layer is assembled to an assembling member, and

the third conductor layer is assembled to a bonding pad of the semiconductor chip.

29. A method of manufacturing a semiconductor device according to claim 28, wherein the first conductor layer is formed so that it covers an entire inner wall of the groove portion.

30. A method of manufacturing a semiconductor device according to claim 28, wherein an interface of the first and the second conductor layer is located above the surface of the lead frame body.

31. A method of manufacturing a semiconductor device according to claim 28, wherein the lead includes a barrier layer for suppressing a reaction between the lead frame body and the first conductor layer, the barrier layer being provided between the first conductor layer and the groove portion, the method of manufacturing a semiconductor device further comprising a step of removing the barrier layer by means of etching after the completion of resin sealing.